

Interference Search History Printout

7/24/05 M.T

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(semiconductor and core and voltage\$1ground near plane and top near signal near plane and dielectric and footpring near image and carrier and circuit near lines and microvias and conductive near via\$1).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/07/24 09:08
L2	0	(core and voltage/ground near plane and top near signal near plane and dielectric and footpring near image and carrier and circuit near lines and microvias and conductive near via\$1).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/07/24 09:09
L3	0	(core and voltage/ground near plane and top near signal near plane and dielectric and footpring near image and carrier and microvias and via\$1).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/07/24 09:09
L4	0	(core and voltage/ground near plane and top near signal near plane and dielectric and footpring near image and carrier).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/07/24 09:09